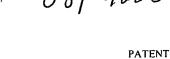
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## ABSTRACT OF THE DISCLOSURE

A display device with a power interruption delay function, comprising a power interruption delay charging circuit for gradually lowering an input voltage to an horizontal/vertical processor constant voltage circuit when power supply to the display device is interrupted. According to the present invention, when power supply is resumed under the condition that a high voltage charged on a horizontal deflection coil and an S-correction capacitor is not sufficiently discharged after power interruption, the power interruption delay charging circuit can prevent a horizontal output transistor from being damaged due to an instantaneous surge current resulting from the high voltage being not discharged.